Electronic Pate	ent App	lication Fee	Transm	ittal			
Application Number:	100	10840125					
Filing Date:	06-	06-May-2004					
Title of Invention:	No	Notched spacer for CMOS transistors					
First Named Inventor/Applicant Name:	Ste	Steve Ming Ting					
Filer:	Ma	Mark E Courtney/Sherry Colgrove					
Attorney Docket Number:	TSI	TSM03-0945					
Filed as Large Entity	•						
Utility under 35 USC 111(a) Filing Fees							
Description		Fee Code	Quantity	Amount	Sub-Total in USD(\$)		
Basic Filing:							
Pages:							
Claims:							
Miscellaneous-Filing:							
Petition:							
Patent-Appeals-and-Interference:							
Post-Allowance-and-Post-Issuance:							
Extension-of-Time:							
Extension - 2 months with \$0 paid		1252	1	490	490		

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Miscellaneous:				
Request for continued examination	1801	1	810	810
	Total in USD (\$)			1300